

## IN THE SPECIFICATION

Please amend the paragraph at page 123, line 1, previously amended in the response filed December 19, 2005, as follows:

An example is shown in Figure 182. In Figure 182(a) (first field), a turn-on voltage is applied to the gate signal line 17b for each pixel row for one horizontal scanning period (1 H) in every four horizontal scanning periods (4 Hs). In Figure 182(b) (second field), a turn-on voltage is applied to the gate signal line 17 for each pixel row for 2Hs in every 4 Hs. Thus, in two fields, a turn-on voltage is applied for (1 + 2) Hs in every (4 + 4) Hs. However, in a unit period (two [[a]] fields in Figure 132), a turn-on voltage is applied to every gate signal line 17b for the same period. Thus, every pixel row is displayed at the same brightness (assuming a white raster display).